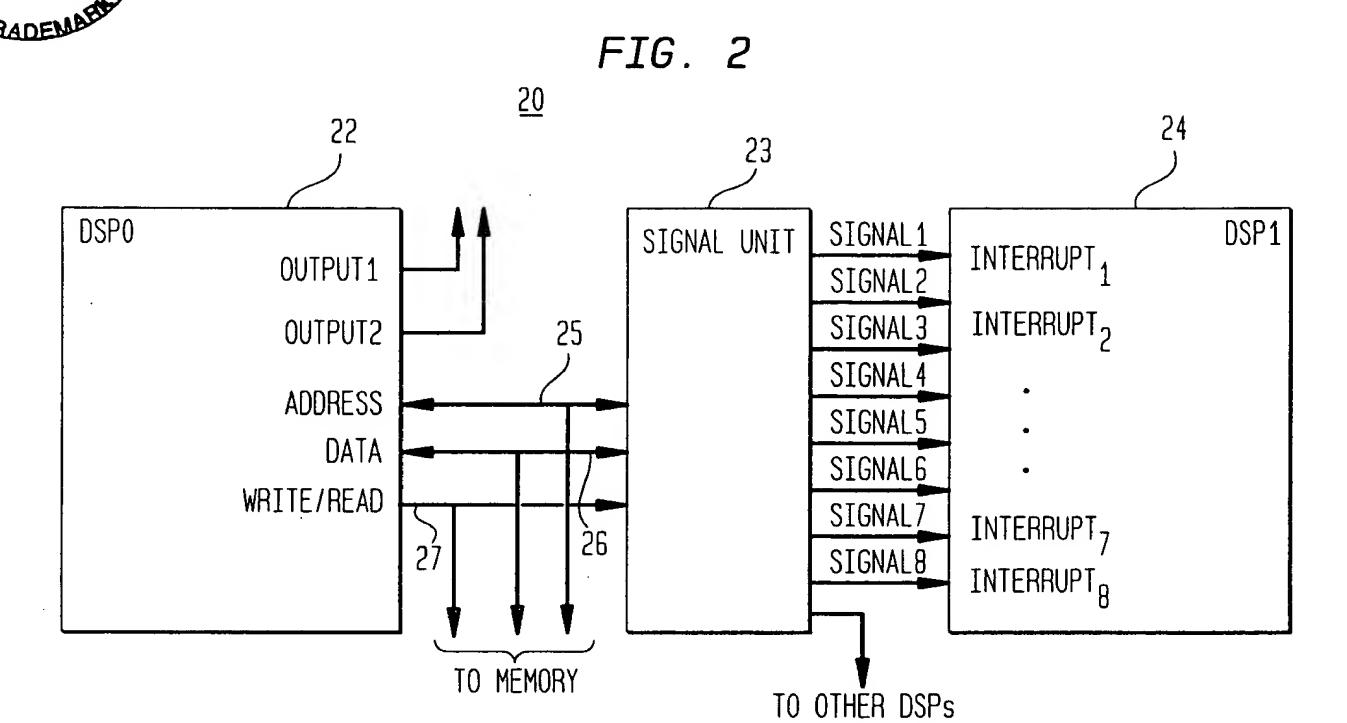
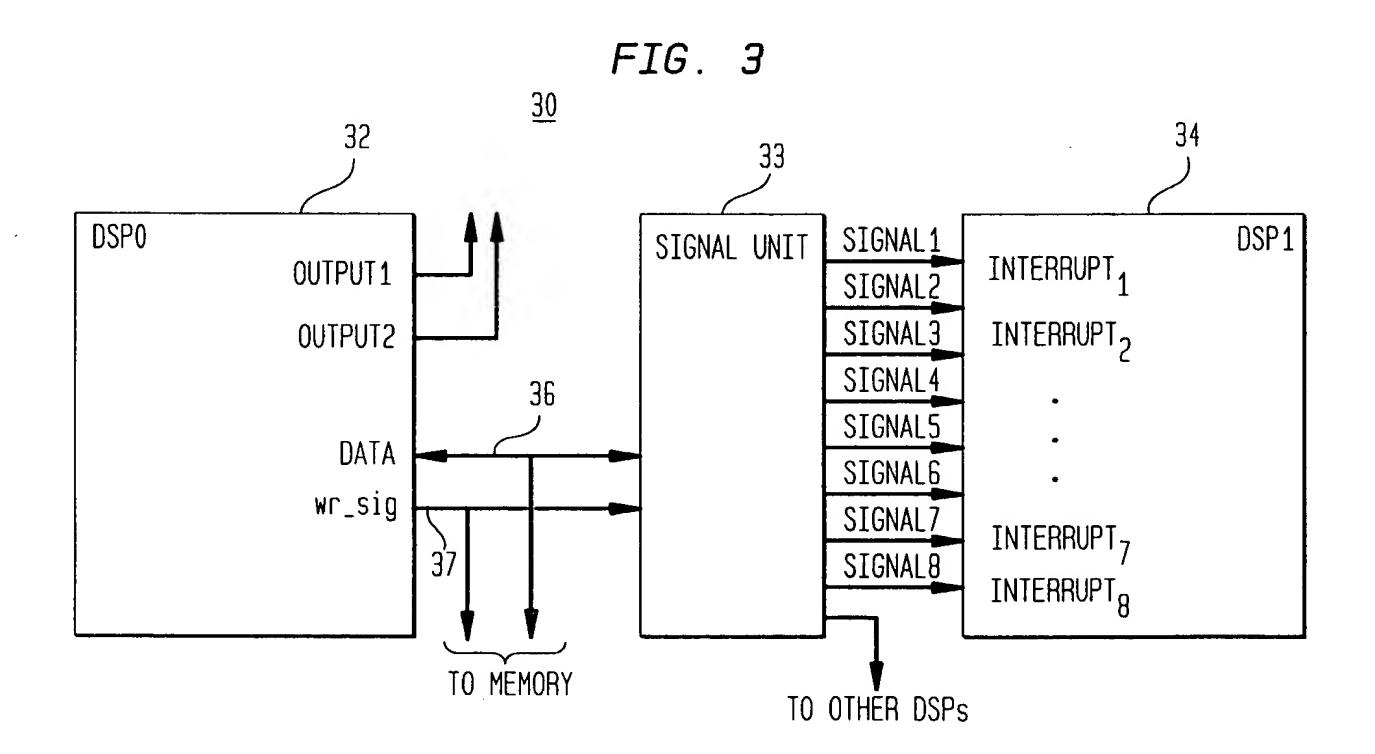
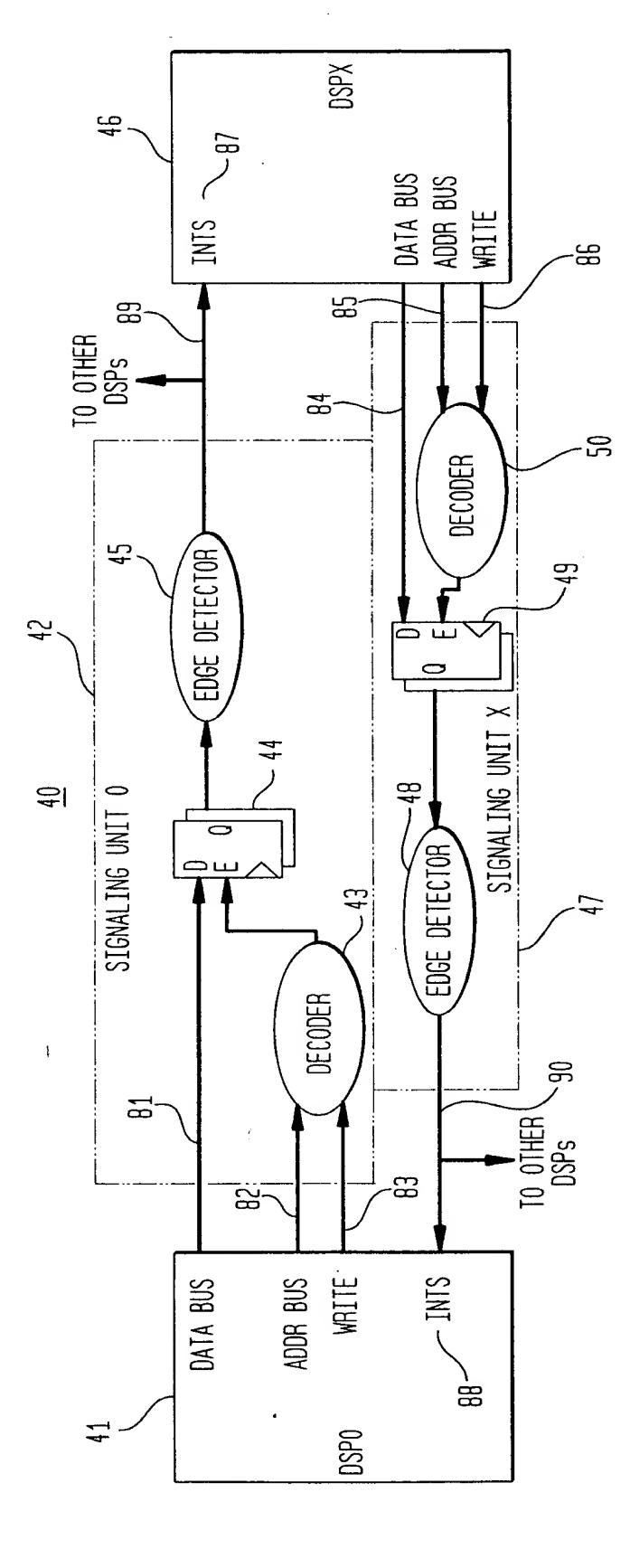


Inventor: William G. Burroughs et al.
Title: Inter-DSP Signaling in a Multiple DSP Environment
Appln. No. 09/489,652 Filed: January 24, 2000
Docket No. LUC-718US Customer #23122
REPLACEMENT SHEET

FIG. 1
(PRIOR ART) <u>10</u> 12 SIGNAL 1 INTERRUPT₁ DSP1 DSP0 OUTPUT1 SIGNAL 2 INTERRUPT₂ OUTPUT2 ADDRESS DATA WRITE/READ TO MEMORY









Inventor: William G. Burroughs et al.
Title: Inter-DSP Signaling in a Multiple DSP Environment
Appln. No. 09/489,652 Filed: January 24, 2000
Docket No. LUC-718US Customer #23122

REPLACEMENT SHEET

FIG. 5

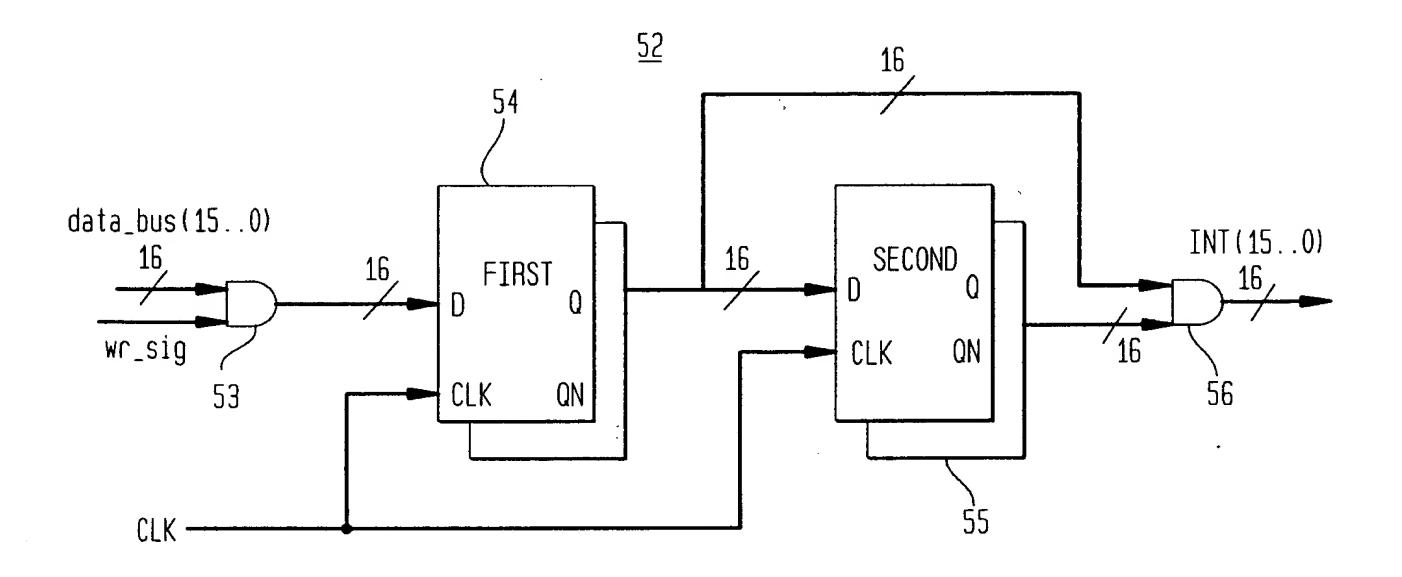
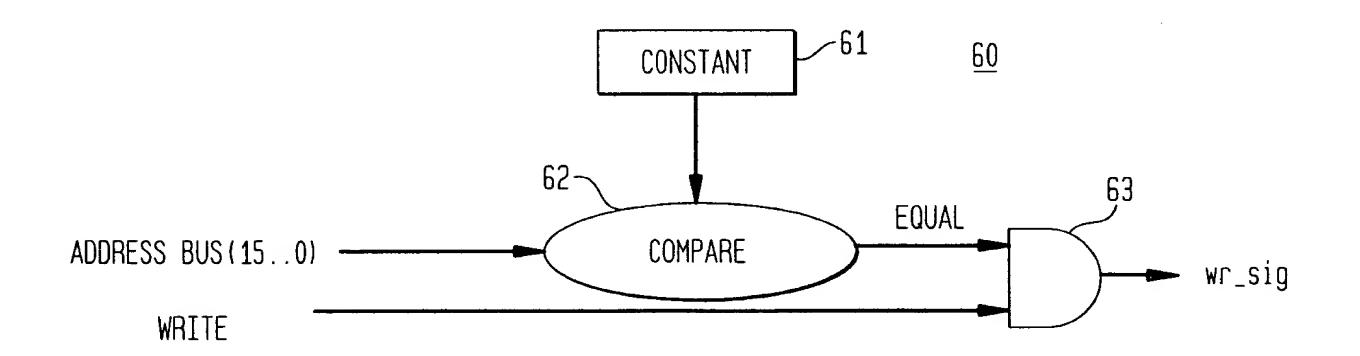


FIG. 6



INT (15..0) SECOND ON OUTPUT (b) data_bus (15..0) FIRST DINPUT FIRST 0 OUTPUT (h) INI (n) (i) INI (1) (c) Wr_sig (a) CLOCK (â) (9 (e)